

US Department of Commerce
Patent and Trademark Office

Atty. Docket No.:

42390P5193C

Application No.:

09/852,217

Applicant:

Roussel et al.

Filing Date:

May 8, 2001

Form PTO-1449 (Modified)

US Patent Documents

Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
ES	09/01/98	5,802,336	Peleg et al.	395	376	01/27/97
EL	09/08/98	5,806,049	Petruzzi	705	36	09/30/96
GL	06/24/97	5,642,306	Mennemeir	364	757	05/15/96
EL	06/20/95	5,426,598	Hagihara	364	759	02/22/94
EL	07/04/72	3,675,001	Singh	235	175	12/10/70
EL	05/11/93	5,210,711	Rossmere et al.	364	786	02/26/92
EL	08/11/98	5,793,661	Dulong et al.	364	750.5	12/26/95
EL	08/10/99	5,936,872	Fischer et al.	364	754.03	12/20/95
EL	03/27/73	3,723,715	Chen et al.	235	175	08/25/71
EL	05/10/94	5,311,508	Buda et al.	370	60	12/23/91
EL	02/24/98	5,721,892	Peleg et al.	395	562	11/06/95
EL	05/21/00	6,041,403	Parker et al.	712	210	09/27/96
EL	01/25/00	6,018,351	Mennemeir et al.	345	437	10/30/97

Foreign Patent Documents

Examiner's Initials	Date	Document Number	Country	Class	Sub- Class	Translation
EL	06/26/97	WO 97/22923	PCT	G06F	9/34	
EL	06/26/97	WO 97/22924	PCT	G06F	9/34	
EL	06/27/97	WO 97/22921	PCT	G06F	9/00	
ES	03/06/97	WO 97/08608	PCT	G06F	7/00	
EL	10/28/99	GB 9907221.7	United Kingdom	G06F	9/302	

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

EL	Abbott et al., "Broadband Algorithms with the MicroUnity Mediaprocessor", Proceedings of COMPCON '96, 1996, pages 349-354.
EL	Hayes et al., "MicroUnity Software Development Environment", Proceedings of COMPCON '96, 1996, pages 341-348.

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Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw a line through the citation if not in conformance and not considered. Include a copy of this form with the next communication to the applicant

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EC	12/26/89	4,890,218	Bram	364	200	08/10/88
EC	09/30/97	5,673,427	Brown et al.	395	595	07/03/96
EC	12/22/98	5,852,726	Lin et al.	395	376	12/19/95
EC	06/30/87	4,677,582	Nagafuji	364	749	04/14/83
EC	07/26/88	4,760,545	Inagami et al.	364	200	12/21/84
EC	01/15/91	4,985,848	Pfeiffer et al.	364	518	09/14/87
EC	03/07/89	4,811,269	Hirose et al.	364	754	10/08/86
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EC	03/02/76	3,941,990	Rabasse et al.	235	176	01/02/74

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EC	Samsung Electronics, "21164 Alpha Microprocessor Data Sheet", 1997.
EC	Silicon Graphics, "Silicon Graphics Introduces Enhanced MIPS Architecture to Lead the Interactive Digital Revolution", 10/21/96, 2 pages.
EC	Philips Electronics, "TriMedia TM1000 Preliminary Data Book", 1997.
EC	"MIPS Digital Media Extension", pp. C1 to C40.
EC	"MIPS Extension for Digital Media with 3D", MIPS Technologies, Inc. March 12, 1997, pp. 1-26.
EC	"The VIS™ Instruction Set", Sun Microsystems, Inc. 1997, pp. 1-2.

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EC		"64-Bit and Multimedia Extensions In the PA-RISC 2.0 Architecture", Hewlett Packard, downloaded from the Website rblee@cup.hp.com.huck@cup.hp.com, pp. 1-8.
EC		"ULTRASPARC™ The Visual Instruction Set (VIS™): On Chip Support for New-Media Processing", Sun Microsystems, Inc. 1996, pp. 1-7.
EC		"ULTRASPARC™ and New Media Support Real-Time MPEG2 Decode with the Visual Instruction Set (VIS™), Sun Microsystems, 1996, pp. 1-8.
EC		ULTRASPARC™ Ultra Port Architecture (UPA): The New-Media System Architecture, Sun Microsystems, Inc. 1996, pp.1-4.
EC		ULTRASPARC™ Turbocharges Network Operations on New Media Computing, Sun Microsystem, Inc., 1996, pp. 1-5.
EC		"The ULTRASPARC Processor - Technology White Paper, Sun Microsystems, Inc. 1995, 37 pages.
EC		"AMD-3D™ Technology Manual, Advanced Micro Devices, February 1998.
EC		Hansen, Craig, "Architecture of a Broadband Mediaprocessor, MicroUnity Systems Engineering, Inc., 1996, pp. 334-354.
EC		Levinthal, Adam, et al. "Parallel Computers for Graphics Applications, Pixar, San Rafael, CA, 1987, pp. 193-198.
EC		Levinthal, Adam; Porter, Thomas, "Chap-A SIMD Graphics Processor", Computer Graphics Project, Lucasfilm Ltd., 1984, pp. 77-82.
EC		Wang, Mangaser, Shrinivan, "A procesor Architecture for 3D Graphics Calculations, Computer Motion, Inc., pp. 1-23.
EC		Visual Instruction Set (VIS™), User's Guide, Sun Microsystems, Inc., verison 1.1 March, 1997.

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EC	09/17/85	4,542,476	Nagafuji et al.	364	749	08/03/82
ES	08/19/75	3,900,724	McIver	235	164	02/11/74

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EC	"Silicon Graphics Introduces Compact MIPS® RISC Microprocessor Code for High Performance at a Low Cost", October 21, 1996, downloaded from Website webmaster@www.sgi.com, pp. 1-2.
ES	Killian, Earl, "MIPS Extension for Digital Media", Silicon Graphics, pp. 1-10.
ES	"MIPS V Instruction Set", pp. B1 to B-37.

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